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LSI Logic Corporation
1551 McCarthy Boulevard
Milpitas, CA 95035

In re Application of

Aronowitz, Zubkov, and Sun

Application No.: 10/698,169

Filed: October 31, 2003

Attorney Docket No:02-6037/LSI1P218

For: **MEMORY DEVICE HAVING AN ELECTRON TRAPPING
LAYER IN A HIGH-DIELECTRIC GATE STACK**

This is in response to the petition under 37 CFR 1.47(a), filed October 31, 2003, and in duplicate on September 7, 2005.

The petition is **GRANTED**.

The above-identified application and papers have been reviewed and found in compliance with 37 CFR 1.47(a). This application is hereby accorded Rule 1.47(a) status.

The above-cited application was filed on October 31, 2003, with a declaration executed by only two of the three named joint inventors, and the petition under 37 CFR 1.47(a). Petitioner has shown that inventor Aronowitz has refused to join the prosecution of the application. The above-identified application and papers have been reviewed and found in compliance with 37 CFR 1.47(a). This application is hereby accorded Rule 1.47(a) status.

As provided in Rule 1.47(c), this Office will forward notice of this application's filing to the non-signing inventor at the addresses given in the petition. Notice of the filing of this application will also be published in the Official Gazette.

The fee for the instant petition on the date it was first filed was \$130.00. Deposit account 12-2252 will be charged an additional \$130.00, accordingly.

This application will be directed to the Office of Patent Publications for further processing.

Telephone inquiries regarding this decision should be directed to the undersigned at (571) 272-3222.

Kenya A. McLaughlin
Petitions Attorney
Office of Petitions